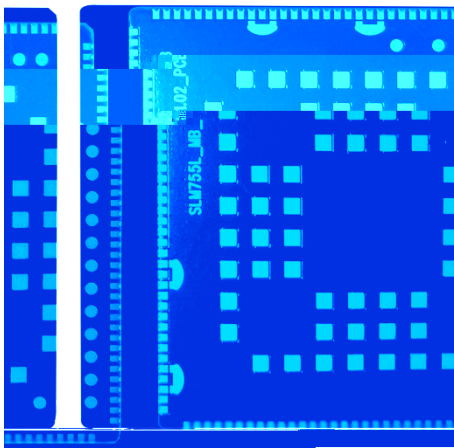
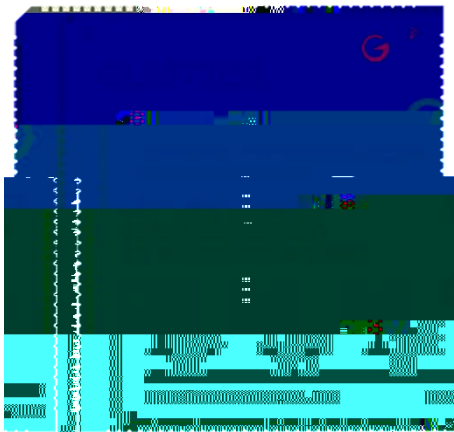


SLM755(SLM755LC)

_V1.02



	SLM755(SLM755LC)
	V1.02
	2018 6

1.

```

I BC- ++ I BC- ++B9          C I C. /&/
      . 8      8      , 8 ( 8      B ;
: B ; :: B ; M9: C7 : I9: C7 ;L: E 9: C7 I C
FI      8
7      )&*      F      -(&F
      F E      E

```

```

I BC- ++ I BC- ++B9
:: B ; 0 - )&C
:: B ; 0 +&C      +&C
M9: C7      : 9 I F7 0*(C      + -, C
;L: E      ;L: E H 70) C      . C
: I9: C7      I F7 0*(C      ( (C
9: C7 0 +) ,      +) ,
I C      ;: ; 0() , .      () , .

```

```

I BC- ++ I BC- ++B9
8      FI

```

M

2.

	G C I C. /&/
9F	G 7-)(
F	7)&* *&/ , C
	. 8 C C 9 8 B F: : H) , 8 (8
	7 +
	*& + *& + (. (&
	*
M	;;; 8(*
8	

	OC F :I *	
9	0 OC F 9I &(1 OC F 9I	
	F OC 0 +C . C	
	L	& &)& 0 ;L9 (,* CF* : N L F. ML 7)& 0 (,)
	L	-(&)& 0 (,* ML 7)& 0 F. CF*
	F	
H		
	L 87	))L *(L) . L
	I: E	9)(8
	I 8	E EH9; I 8 8EE I 8
	7H (	* (
	(9 *	F
	I F	C
	7: 9 (	
	F E	(*
	L9E D	

(C 9 ;9C C;CI C 9

3. SLM755(SLM755LC)

36	UART1_CTS	B-PD:nppukp	Configurable I/O UARTor I2C SDA
37	UART1_RTS	B;B-PD:nppukp	Configurable I/O UARTor I2C SCL
38	VREG_L11_SDC	PO	PMIC output 2.95V
39	SDC2_SDCARD_CLK	BH-NP:pdpukp	Secure digital controller 2 clock
40	SDC2_SDCARD_CMD	BH-PD:nppukp	Secure digital controller 2 command
41	SDC2_SDCARD_D0	BH-PD:nppukp	Secure digital controller 2 data bit 0
42	SDC2_SDCARD_D1	BH-PD:nppukp	Secure digital controller 2 data bit 1
43	SDC2_SDCARD_D2	BH-PD:nppukp	Secure digital controller 2 data bit 2
44	SDC2_SDCARD_D3	BH-PD:nppukp	Secure digital controller 2 data bit 3
45	SDC2_SDCARD_DET	B-PD:nppukp	Configurable I/O ,SD_DET_N
46	FORCE_USB_BOOT	B-PD:nppukp	pullup to 1.8v will force MSM to boot from USB_HS port
47	TP_I2C_SCL	B;B-PD:nppukp	Configurable I/O CTP I2C
48	TP_I2C_SDA		

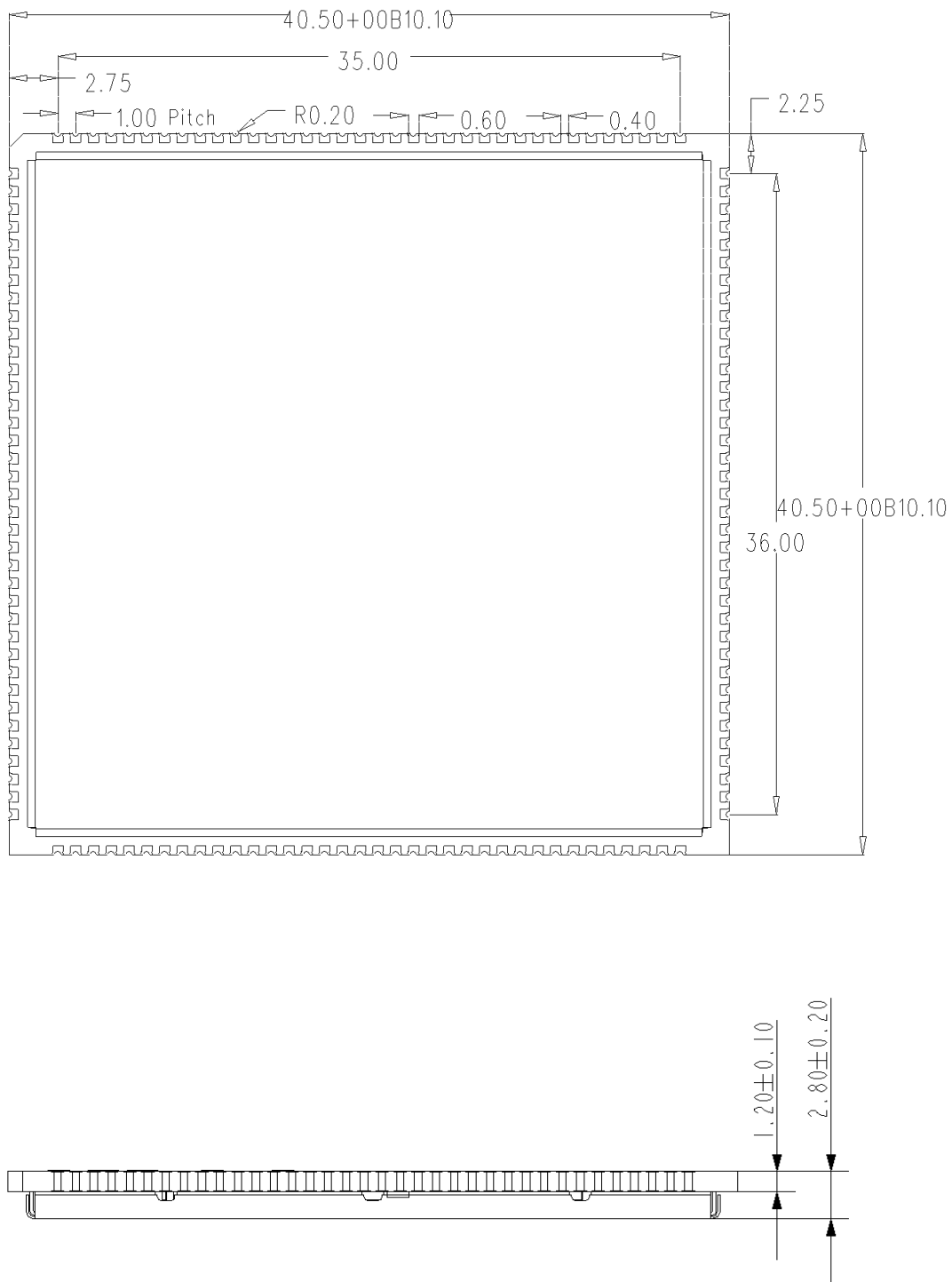
76	GND	GND	GND
77	RF_WIFI/BT	AI,AO	RF signal
78	GND	GND	GND
79	CAM0_RST_N	DO;B-PD:nppukp	Rear camera reset; Configurable I/O
80	CAM0_PWDN	DO;B-PD:nppukp	Rear camera pwn Configurable I/O
81	CAM1_RST_N	DO;B-PD:nppukp	Front camera reset Configurable I/O
82	CAM1_PWDN	DI;B-PD:nppukp	Front camera pwn Configurable I/O
83	CAM_I2C_SCL	B-PD:nppukp	Camera I2C_SCL,can't be used for other
84	CAM_I2C_SDA	B-PD:nppukp	Camera I2C_SDA,can't be used for other
85	GND	GND	GND
86	GND	GND	GND
87	RF_MAIN	AI,AO	RF signal-Main ANT
88	GND	GND	GND
89	GND	GND	GND
90	GPIO32	B-PD:nppukp	Configurable I/O
91	GPIO7_I2C_SCL	DO;B-PD:nppukp	Configurable I/O I2C or GPIO
92	GPIO6_I2C_SDA	DO;B-PD:nppukp	Configurable I/O I2C or GPIO
93	UART2_MSM_RX	B;B-PD:nppukp	Configurable I/O UART for debug
94	UART2_MSM_TX	BD;B-PD:nppukp	Configurable I/O UART for debug
95	GPIO90_KEY_VOL_UP	DI;B-PD:nppukp	Configurable I/O Keypad sense bit 0
96	GPIO91_KEY_VOL_DOWN	DI;B-PD:nppukp	Configurable I/O Keypad sense bit 1
97	GPIO31	B-PD:nppukp	Configurable I/O
98	GPIO92	B-PD:nppukp	Configurable I/O
99	GPIO88	B-PD:nppukp	Configurable I/O
100	GPIO89	B-PD:nppukp	Configurable I/O
101	GPIO69	B-PD:nppukp	Configurable I/O
102	GPIO68	B-PD:nppukp	Configurable I/O
103	GPIO97	B-PD:nppukp	Configurable I/O
104	GPIO110	B-PD:nppukp	Configurable I/O
105	GPIO0	B-PD:nppukp	Configurable I/O
106	GPIO98	B-PD:nppukp	Configurable I/O
107	GPIO94	B-PD:nppukp	Configurable I/O
108	GPIO36	B-PD:nppukp	Configurable I/O
109	GPIO65	B-PD:nppukp	Configurable I/O
110	GPIO96	B-PD:nppukp	Configurable I/O
111	VREG_L5_1P8	OUPUT	PMU Supply 1.8V
112	GPIO58	B-PD:nppukp	Configurable I/O
113	GPIO99	B-PD:nppukp	Configurable I/O
114	KYPD_PWR_N	DI	Power on key
115	GPIO95	B-PD:nppukp	Configurable I/O

116	GPIO11_SPI_CLK	B-PD:nppukp	Configurable I/O SPI or I2C
117	GPIO10_SPI_CS	B-PD:nppukp	Configurable I/O SPI or I2C
118	GPIO9_SPI_MISO	B-PD:nppukp	Configurable I/O SPI
119	GPIO8_SPI_MOSI	B-PD:nppukp	Configurable I/O SPI
120	GND	GND	GND
121	RF_GPS	AI	RF signal-GPS ANT
122	GND	GND	GND
123	GPIO16	B-PD:nppukp	Configurable I/O,
124	GPIO17	B-PD:nppukp	Configurable I/O,
125	VREG_L6_1P8	OUPUT	PMU Supply 1.8V
126	VCOIN	PI	VCOIN
127	CHARGE_SEL	DI	SMB CHAGER SEL
128	PM8909_MPP4	AO-Z;DI	Configurable MPP; used for ADC IN
129	VREG_L17_2P85	OUPUT	PMU Supply 2.85V
130	GND	GND	GND
131	RF_DIV	AI	RF signal-DIV ANT
132	GND	GND	GND
133	VBATT_SNS	PI	Battery voltage sense point
134	BAT_THERM	DI	Battery therm monitor
135	GND	GND	GND
136	CDC_HPH_R	AO	Earphone right output
137	CDC_HPH_REF	AI	Earphone driver amplifier ground reference
138	CDC_HPH_L	AO	Earphone left output
139	CDC_HS_DET	DI	Headset detection
140	GND	GND	GND
141	VBUS	PI	USB VBUS Voltage
142	VBUS	PI	USB VBUS Voltage
143	GND	GND	GND
144	GND	GND	GND
145	VBAT	PI,PO	Battery
146	VBAT	PI,PO	Battery
147	GND	GND	GND
148	GND	GND	GND
149	GND	GND	GND
150	GND	GND	GND
151	MIC3_IN3_P		Microphone 3 input, single-ended
152	MIC_BIAS1		MIC1 bias power supply
153	MIC_BIAS2		MIC2 bias power supply
154	RESERVED		RESERVED
155	RESERVED		RESERVED
156	RESERVED		RESERVED

157	RESERVED		RESERVED
158	RESERVED		RESERVED
159	RESERVED		RESERVED
160	GND	GND	GND
161	GND	GND	GND
162	GND	GND	GND
163	GND	GND	GND
164	GND	GND	GND
165	GND	GND	GND
166	GND	GND	GND
167	GND	GND	GND
168	GND	GND	GND
169	GND	GND	GND
170	GND	GND	GND
171	GND	GND	GND
172	GND	GND	GND
173	GND	GND	GND
174	GND	GND	GND
175	GND	GND	GND
176	GND	GND	GND
177	GND	GND	GND
178	GND	GND	GND
179	PM_RESIN_N	DI	PMIC reset
180	GND	GND	GND
181	GND	GND	GND
182	GND	GND	GND
183	RESERVED		RESERVED
184	GND	GND	GND
185	GND	GND	GND
186	GND	GND	GND
187	RESERVED		RESERVED
188	GND	GND	GND
189	GND	GND	GND
190	RF_FM	AO	RF signal-FM ANT
191	RESERVED		RESERVED
192	GND	GND	GND
193	GND	GND	GND
194	RESERVED		RESERVED
195	RESERVED		RESERVED
196	RESERVED		RESERVED
197	RESERVED		RESERVED

198	GND	GND	GND
199	RESERVED		RESERVED
200	GND	GND	GND
201	GND	GND	GND
202	RESERVED		RESERVED
203	RESERVED		RESERVED
204	RESERVED		RESERVED
205	RESERVED		RESERVED
206	RESERVED		RESERVED
207	CHG_LED_SINK		Negative Power charging indicator isink
208	GND	GND	GND
209	GND	GND	GND
210	CBL_PWR_N	DI	Cable poweron detect input

4. 2D



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